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APPLICANTS Jed H. Rankin, South Burlington, VT; Andrew J. Watts, Essex, VT;				
** CONTINUING DATA ***** None lpd				
** FOREIGN APPLICATIONS ***** None lpd				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 09/16/2004				
Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		STATE OR COUNTRY VT	SHEETS DRAWING 8	TOTAL CLAIMS 27
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				INDEPENDENT CLAIMS 4
Verified and Acknowledged Examiner's Signature: <i>H. O. G.</i> Initials: <i>HD</i>				
ADDRESS 30449				
TITLE Method to selectively correct critical dimension errors in the semiconductor industry				
FILING FEE RECEIVED 1282	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	